

REMARKS

Applicant wishes to thank the Examiner for reviewing the present application and the detailed nature of those comments that has facilitated the preparation of the response to the Office Action.

In the revised claim 1, amendments have been made to provide correct antecedent basis for the terms used in the claim. In addition, it will be noted that claim 1 has been amended to incorporate the additional step of selecting a state of the computing apparatus at (c) (iv).

Claim 1 has also been amended to include the recitation of the method being performed on a computing apparatus and thereby clearly recites patentable subject matter under Section 101.

In claim 2 amendments have been made to overcome the objection with respect to the multiplicative inverse. Similarly amendments have been made to claim 4 as suggested by the Examiner with the additional explanation that the key is used in a cryptosystem to provide context.

Claim 6 has been revised for consistency as requested by the Examiner. New claim 7, 8 and 9 have been added dependent on revised claim 1 directed to further features of the invention.

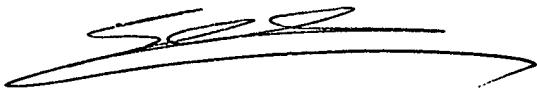
With respect to the drawings, formal drawings are included herewith for substitution of the drawings previously submitted. A number of minor typographical errors have been corrected on the drawings so as to be consistent throughout the drawings. For example, in Figure 3 as originally filed, the decision line indicated "if i =0." This is inconsistent with the table in Figure 3 where it indicates $i < 0$ for that action.

An amendment has been made on Figure 4 at line 10 to reflect the teachings at page 4, line 30. Thus, line 10 now requires the H state to be set to the compliment of the value of the bit so as to be consistent with the text.

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It is believed that these amendments place the application in condition for allowance and action to that end is respectfully requested.

Respectfully Submitted,



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